

Attorney Docket No.: LOVO-056.DIV

UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Inventor(s):

Ho-Yuan Yu

Application No.:

10/816,980

Group Art Unit:

Filed:

04/02/04

Examiner:

Title:

METHOD AND STRUCTURE FOR COMPOSITE TRENCH FILL

Form 1449

U.S. Patent Documents

	Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
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Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	ation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	В							

Other Documents

Examiner						
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication				
MA	С	H. Ogiwara, M. Hayakawa, T. Nishimura and M. Nakaoka; "HIGH-FREQUENCY INDUCTION HEATING INVERTER WITH MULTI-RESONANT MODE USING NEWLY DEVELOPED NORMALLY-OFF TYPE STATIC INDUCTION TRANSISTORS"; Department of Electrical Engineering, Ashikaga Institute of Technology, Japan; Department of Electrical Engineering, Oita University, Japan; Department of Electrical Engineering, Kobe University, Japan; pages 1017-1023				
MU	D	J. Baliga; "HIGHVOLTAGE JUNCTION-GATE FIELD EFFECT TRANSISTOR WTH RECESSED GATES"; IEEE Transactions on Electron Devices; Vol. ED-29; No. 10; Oct. 1982				
· MM	Ε	J. M. C. Stork et al.; "SMALL GEOMETRY DEPLETED BASE BIPOLAR TRANSISTORS (BSIT)- VLSI DEVICES?"; IEEE Transactions on Electron Devices; Vol. ED-28; No. 11; Nov. 1981				
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My	G	Caruso et al.; "PERFORMANCE ANALYSIS OF A BIPOLAR MODE FET (BMFET) WITH NORMALLY OFF CHARACTERISTICS"; IEEE Transactions on Power Electronics; Vol. 3; No. 2; April 1988				
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Examiner	IN	Date Considered 4 14 05				

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.